



15.17/5051

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of: FURUHATA et al.)
Serial No.: 09/599,477)
Filed: June 23, 2000)
For: SEMICONDUCTOR DEVICES)
INCLUDING A MULTI-WELL AND)
SPLIT GATE NON-VOLATILE)
MEMORY TRANSISTOR STRUCTURE)

Group Art Unit: 2826

Examiner: Dickey, T.

TECHNOLOGY CENTER 2800

FEB 27 2003

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Box RCE
Commissioner for Patents
Washington, D. C. 20231

Dear Sir:

In accordance with Rule 1.136, the applicant respectfully petitions the Commissioner for a one month extension of time extending the period for response to the Notice of Appeal mailed to the Office by applicant on Nov. 18, 2002 and apparently entered by the Office on Nov. 22, 2002. The \$110 fee and responsive papers are attached. If it should be determined that a longer extension of time is required to prevent this application from becoming abandoned, or for any other reason an insufficient fee has been paid, please charge any insufficiency to Deposit Account No. 50-0585. **A duplicate copy of this petition is enclosed.**

Respectfully submitted,

Alan S. Raynes

Reg. No. 39,809

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Dated: February 18, 2003

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Box RCE, Commissioner for Patents, Washington, D.C. 20231 on February 18, 2003.

Alan S. Raynes, Reg. No. 39,809

Name of Applicant, assignee, or Registered Rep.

Alan S. Raynes

2/18/03

Date

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